



IEE Cambridge One Day Seminar

Processor Cores Putting Silicon IP to Work

5th December 2005
Computer Laboratory
University of Cambridge

Time	Title	Speaker
09:00	Registration	
09:30	Welcome and Introduction	Chairman IEE Cambridge Branch Committee
09:40	Keynote Address: <i>The Return of Innovation</i>	Prof. David May University of Bristol
10:20	ARC's Customisable Processors	Kami Sehat ARC
11:00	Coffee	
11:30	ARM Cortex Processor Family	Richard Grisenthwaite ARM
12:10	The Firepath LIW Processor and ISA Extensions for Broadband	Sophie Wilson Broadcom
12:50	Buffet Lunch	
14:00	XAP3 and XAP4 - Royalty-free Processors for ASICs and FPGAs	Alistair Morfey Cambridge Consultants Ltd
14:40	Structured ASICs and their use in Embedded IP SOC Designs	Dipak Raval LSI Logic
15:10	Tea	
15:50	Is There a Parallel Universe?	Will Robbins PicoChip
16:30	The SoC Future is Soft	Simon Knowles Icera Semiconductor
17:10	Close of Seminar	

For registration and further details see:
<http://www.iee-cambridge.org.uk/seminar/>